



# GD32F1xx CPU Support Package Guide

**Version: 3.2**





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# GD32F1xx Support Package

This guide describes the following features of the GD32F1xx CPU support package:

- [How to create GD32F1xx projects](#)
- [How to open GD32F1xx sample projects](#)
- [GD32F1xx specific project properties](#)
- [GD32F1xx specific project templates](#)
- [Supported GD32F1xx devices](#)

# Creating GD32F1xx Projects

## Creating an GD32F1xx C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for GigaDevice GD32F1xx** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

## Creating an GD32F1xx library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for GigaDevice GD32F1xx** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

## Creating an GD32F1xx externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for GigaDevice GD32F1xx** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

## Creating an GD32F1xx CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for GigaDevice GD32F1xx** project template.
- Set the required project name and location directory.
- Click **Next**.



If required, change any of the other default project settings.  
Click **Finish** to create the project.

### Creating an GD32F1xx assembly code only executable project

*Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.*

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.

Select the **An assembly code only executable for GigaDevice GD32F1xx** project template.

Set the required project name and location directory.

Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

# Opening GD32F1xx Sample Solutions

## GD32F1xx Samples Solution

This solution contains general sample projects that run on GD32F1xx devices. To open the GD32F1xx Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **GigaDevice GD32F1xx CPU Support Package** link.
- Select the **Samples Solutions > GD32F1xx Samples Solution** link.

## GD32F1xx CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on GD32F1xx devices. To open the GD32F1xx CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **GigaDevice GD32F1xx CPU Support Package** link.
- Select the **Sample Solutions > GD32F1xx CMSIS-DSP Samples Solution** link.

# GD32F1xx Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

## Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

## Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For GD32F1xx projects, the set of placements are:

**Flash** - The application runs in internal Flash memory (*default*).

**Flash Vectors In RAM** - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

**Flash Copy To RAM** - The application starts in internal flash and copies itself to run from internal RAM memory.

**RAM** - The application runs from internal RAM memory only.

## Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

## Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For GD32F1xx projects, the **Startup From Reset** project property can be set to one of the following:

**No** - The application will not startup from reset.

**Release Only** - The application will only startup from reset when built in *Release* configuration (*default*).

**Yes** - The application will always startup from reset.

## Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [GD32F1xx Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

## GD32F1xx Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="GD32F1xx_EXE">
    <configuration Name="Common" package_dependencies="GD32F1xx" Target="GD32F103RK" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="GD32F1xx_EXE">
    <configuration Name="Common" package_dependencies="GD32F1xx" Target="GD32F103RK"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

### Available GD32F1xx project templates

Template Name	Template Description
GD32F1xx_ASM_EXE	GD32F1xx Assembly Code Only Executable
GD32F1xx_CTL_EXE	GD32F1xx CTL Executable
GD32F1xx_EXE	GD32F1xx C/C++ Executable
GD32F1xx_EXT_EXE	GD32F1xx Externally Built Executable
GD32F1xx_LIB	GD32F1xx Library

## GD32F1xx Devices

This package supports the following GD32F1xx devices:

[GD32F101 Family](#)

[GD32F103 Family](#)

[GD32F105 Family](#)

[GD32F107 Family](#)

## GD32F101 Family

GD32F101T4  
GD32F101T6  
GD32F101T8  
GD32F101TB  
GD32F101C4  
GD32F101C6  
GD32F101R4  
GD32F101R6  
GD32F101R8  
GD32F101RB  
GD32F101C8  
GD32F101CB  
GD32F101V8  
GD32F101VB  
GD32F101RC  
GD32F101VC  
GD32F101ZC  
GD32F101RD  
GD32F101VD  
GD32F101ZD  
GD32F101RE  
GD32F101VE  
GD32F101ZE  
GD32F101RF  
GD32F101VF  
GD32F101ZF  
GD32F101RG  
GD32F101VG  
GD32F101ZG  
GD32F101RI  
GD32F101VI  
GD32F101ZI  
GD32F101RK  
GD32F101VK  
GD32F101ZK

## GD32F101T4

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101T4_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101



## GD32F101T6

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101T6_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08007FFF
RAM	0x20000000 - 0x200017FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

# GD32F101T8

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101T8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x200027FF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

## GD32F101TB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101TB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101C4

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101C4_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH 0x08000000 - 0x08003FFF

RAM 0x20000000 - 0x20000FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

## GD32F101C6

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101C6_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08007FFF
RAM	0x20000000 - 0x200017FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101R4

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101R4_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101R6

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101R6_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08007FFF
RAM	0x20000000 - 0x200017FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101R8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101R8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x200027FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101



# GD32F101RB

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101C8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101C8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x200027FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101CB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101CB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101V8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101V8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x200027FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101VB

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20003FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101RC

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101VC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

# GD32F101ZC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101



## GD32F101RD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x2000BFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101VD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x2000BFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

# GD32F101ZD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x2000BFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

# GD32F101RE

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000BFFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101VE

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000BFFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101ZE

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000BFFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101RF

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20013FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101VF

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20013FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101



# GD32F101ZF

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20013FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

## GD32F101RG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20013FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101VG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20013FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101ZG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20013FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

# GD32F101RI

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RI_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x081FFFFFF
RAM	0x20000000 - 0x20013FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F101VI

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VI_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x081FFFFFF
RAM	0x20000000 - 0x20013FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101ZI

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZI_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x081FFFFFF
RAM	0x20000000 - 0x20013FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101

# GD32F101RK

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101RK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F101\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x082FFFFF
RAM	0x20000000 - 0x20013FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F101\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F101



# GD32F101VK

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101VK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x082FFFFF
RAM	0x20000000 - 0x20013FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

# GD32F101ZK

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F101
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F101ZK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F101_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x082FFFFFF
RAM	0x20000000 - 0x20013FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F101_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F101

## GD32F103 Family

GD32F103T4  
GD32F103C4  
GD32F103R4  
GD32F103T6  
GD32F103C6  
GD32F103R6  
GD32F103T8  
GD32F103C8  
GD32F103R8  
GD32F103TB  
GD32F103CB  
GD32F103V8  
GD32F103VB  
GD32F103RB  
GD32F103RC  
GD32F103VC  
GD32F103ZC  
GD32F103RD  
GD32F103VD  
GD32F103ZD  
GD32F103RE  
GD32F103VE  
GD32F103ZE  
GD32F103RF  
GD32F103VF  
GD32F103ZF  
GD32F103RG  
GD32F103VG  
GD32F103ZG  
GD32F103RI  
GD32F103VI  
GD32F103ZI  
GD32F103RK  
GD32F103VK  
GD32F103ZK

## GD32F103T4

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103T4_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08003FFF
RAM	0x20000000 - 0x200017FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103C4

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103C4_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08003FFF
RAM	0x20000000 - 0x200017FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103R4

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103R4_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08003FFF
RAM	0x20000000 - 0x200017FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103T6

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103T6_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x08007FFF
RAM	0x20000000 - 0x200027FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103C6

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103C6_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x08007FFF
RAM	0x20000000 - 0x200027FF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103



## GD32F103R6

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103R6_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x08007FFF
RAM	0x20000000 - 0x200027FF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103T8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103T8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x20004FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103C8

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103C8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x20004FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103R8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103R8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x20004FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103TB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103TB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20004FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103CB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103CB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20004FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103V8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103V8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH 0x08000000 - 0x0800FFFF

RAM 0x20000000 - 0x20004FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103VB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_MD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20004FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103



## GD32F103RB

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_MD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_MD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_MD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20004FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_MD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_MD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

# GD32F103RC

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x2000BFFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103VC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x2000BFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103ZC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x2000BFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103RD

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x2000FFFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103VD

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_HD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x2000FFFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_HD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_HD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103ZD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103RE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103



## GD32F103VE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103ZE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_HD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_HD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_HD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103RF

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

# GD32F103VF

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103ZF

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

## GD32F103RG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103VG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103ZG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103



## GD32F103RI

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RI_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x081FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103VI

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VI_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x081FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F103ZI

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZI_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x081FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

# GD32F103RK

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103RK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_XD

\_\_GD32F103\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x082FFFFF
RAM	0x20000000 - 0x20017FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_XD\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_XD\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F103

# GD32F103VK

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103VK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions
ARM_MATH_CM3
GD32F10X_XD
__GD32F103_SUBFAMILY
__GD32F1xx_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x082FFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily=GD32F1xx
DeviceSubFamily=GD32F103

# GD32F103ZK

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F103
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F103ZK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_XD	
__GD32F103_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x082FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F103_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_XD_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_XD_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F103

## GD32F105 Family

[GD32F105R8](#)

[GD32F105V8](#)

[GD32F105RB](#)

[GD32F105VB](#)

[GD32F105RC](#)

[GD32F105VC](#)

[GD32F105ZC](#)

[GD32F105RD](#)

[GD32F105VD](#)

[GD32F105ZD](#)

[GD32F105RE](#)

[GD32F105VE](#)

[GD32F105ZE](#)

[GD32F105RF](#)

[GD32F105VF](#)

[GD32F105ZF](#)

[GD32F105RG](#)

[GD32F105VG](#)

[GD32F105ZG](#)

## GD32F105R8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105R8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105



## GD32F105V8

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105V8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105RB

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105RB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F105\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x2000FFFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F105\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F105

## GD32F105VB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105VB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F105\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH 0x08000000 - 0x0801FFFF

RAM 0x20000000 - 0x2000FFFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F105\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F105

# GD32F105RC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105RC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105VC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105VC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

# GD32F105ZC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105ZC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105RD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105RD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105VD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105VD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105



# GD32F105ZD

## Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105ZD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F105\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

## Memory Segments

FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x20017FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F105\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F105

## GD32F105RE

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105RE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F105\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F105\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F105

## GD32F105VE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105VE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105ZE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105ZE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105RF

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105RF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105VF

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105VF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105ZF

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105ZF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F105\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F105\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F105

## GD32F105RG

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105RG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F105\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F105\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F105



## GD32F105VG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105VG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F105ZG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F105
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F105ZG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F105_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F105_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F105

## GD32F107 Family

GD32F107RB

GD32F107VB

GD32F107RC

GD32F107VC

GD32F107ZC

GD32F107RD

GD32F107VD

GD32F107ZD

GD32F107RE

GD32F107VE

GD32F107ZE

GD32F107RF

GD32F107VF

GD32F107ZF

GD32F107RG

GD32F107VG

GD32F107ZG

## GD32F107RB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107RB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107VB

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107VB_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F107\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH 0x08000000 - 0x0801FFFF

RAM 0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F107\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F107

## GD32F107RC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107RC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107VC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107VC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107ZC

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107ZC_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107



## GD32F107RD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107RD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107VD

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107VD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F107\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F107\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F107

## GD32F107ZD

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107ZD_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0805FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107RE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107RE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107VE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107VE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107ZE

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107ZE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107RF

### Device Details

CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107RF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F107\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F107\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F107

## GD32F107VF

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107VF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107



## GD32F107ZF

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107ZF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080BFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107RG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107RG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107VG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107VG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
GD32F10X_CL	
__GD32F107_SUBFAMILY	
__GD32F1xx_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
DeviceHeaderFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
DeviceLoaderFile	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
DeviceSystemFile	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
DeviceVectorsFile	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s
DeviceFamily	GD32F1xx
DeviceSubFamily	GD32F107

## GD32F107ZG

Device Details	
CMSIS Header File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h
CMSIS Include Path	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include
CMSIS System File	\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system_gd32f10x.c
Family	GD32F1xx
Sub Family	GD32F107
Loader File	\$(TargetsDir)/GD32F1xx/Loader/GD32F107_Loader.elf
Memory Map File	\$(TargetsDir)/GD32F1xx/XML/GD32F107ZG_MemoryMap.xml
Register Definition File	\$(TargetsDir)/GD32F1xx/XML/GD32F10x_CL_Registers.xml
Vectors File	\$(TargetsDir)/GD32F1xx/Source/GD32F10x_CL_Vectors.s

### Preprocessor Definitions

ARM\_MATH\_CM3

GD32F10X\_CL

\_\_GD32F107\_SUBFAMILY

\_\_GD32F1xx\_FAMILY

### Memory Segments

FLASH 0x08000000 - 0x080FFFFF

RAM 0x20000000 - 0x20017FFF

### Project Macros

DeviceIncludePath=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include

DeviceHeaderFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Include/gd32f10x.h

DeviceLoaderFile=\$(TargetsDir)/GD32F1xx/Loader/GD32F107\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/GD32F1xx/XML/GD32F10x\_CL\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/GD32F1xx/CMSIS/Device/GD32F10x/Source/system\_gd32f10x.c

DeviceVectorsFile=\$(TargetsDir)/GD32F1xx/Source/GD32F10x\_CL\_Vectors.s

DeviceFamily=GD32F1xx

DeviceSubFamily=GD32F107