



LPC800 CPU Support Package Guide

Version: 1.6



Contents

LPC800 Support Package	5
Creating LPC800 Projects	6
Opening LPC800 Sample Solutions	8
LPC800 Project Properties	9
LPC800 Project Templates	11
LPC800 Devices	12
LPC802 Family	13
LPC802M001JDH16	14
LPC802M001JDH20	15
LPC802M011JDH20	16
LPC802M001JHI33	17
LPC802UK	18
LPC804 Family	19
LPC804M101JDH20	20
LPC804M101JDH24	21
LPC804M111JDH24	22
LPC804M101JHI33	23
LPC804UK	24
LPC81x Family	25
LPC810M021FN8	26
LPC811M001JDH16	27
LPC812M101JDH16	28
LPC812M101JD20	29

LPC812M101JDH20	30
LPC812M101JTB16	31
LPC82x Family	32
LPC824M201JHI33	33
LPC824M201JDH20	34
LPC822M101JHI33	35
LPC822M101JDH20	36
LPC83x Family	37
LPC834M101FHI33	38
LPC832M101FDH20	39
LPC84x Family	40
LPC844M201JBD48	41
LPC844M201JBD64	42
LPC844M201JHI33	43
LPC844M201JHI48	44
LPC845M301JBD48	45
LPC845M301JBD64	46
LPC845M301JHI33	47
LPC845M301JHI48	48
LPC8N04 Family	49
LPC8N04FHI24	50



LPC800 Support Package

This guide describes the following features of the LPC800 CPU support package:

- [How to create LPC800 projects](#)
- [How to open LPC800 sample projects](#)
- [LPC800 specific project properties](#)
- [LPC800 specific project templates](#)
- [Supported LPC800 devices](#)

Creating LPC800 Projects

Creating an LPC800 C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for NXP LPC800** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an LPC800 library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for NXP LPC800** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an LPC800 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for NXP LPC800** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an LPC800 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for NXP LPC800** project template.
- Set the required project name and location directory.
- Click **Next**.

If required, change any of the other default project settings.
Click **Finish** to create the project.

Creating an LPC800 assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.
Select the **An assembly code only executable for NXP LPC800** project template.
Set the required project name and location directory.
Click **Next**.
If required, change any of the other default project settings.
Click **Finish** to create the project.

Opening LPC800 Sample Solutions

LPC800 Samples Solution

This solution contains general sample projects that run on LPC800 devices. To open the LPC800 Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **NXP LPC800 CPU Support Package** link.
- Select the **Samples Solutions > LPC800 Samples Solution** link.

LPC800 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on LPC800 devices. To open the LPC800 CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **NXP LPC800 CPU Support Package** link.
- Select the **Sample Solutions > LPC800 CMSIS-DSP Samples Solution** link.

LPC800 Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For LPC800 projects, the set of placements are:

Flash - The application runs in internal Flash memory (*default*).

Flash Vectors In RAM - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

Flash Copy To RAM - The application starts in internal flash and copies itself to run from internal RAM memory.

RAM - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For LPC800 projects, the **Startup From Reset** project property can be set to one of the following:

No - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration (*default*).

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [LPC800 Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

LPC800 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="LPC800_EXE">
    <configuration Name="Common" package_dependencies="LPC800" Target="LPC845M301JBD48" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="LPC800_EXE">
    <configuration Name="Common" package_dependencies="LPC800" Target="LPC845M301JBD48"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available LPC800 project templates

Template Name	Template Description
LPC800_ASM_EXE	LPC800 Assembly Code Only Executable
LPC800_CTL_EXE	LPC800 CTL Executable
LPC800_EXE	LPC800 C/C++ Executable
LPC800_EXT_EXE	LPC800 Externally Built Executable
LPC800_LIB	LPC800 Library

LPC800 Devices

This package supports the following LPC800 devices:

- [LPC802 Family](#)
- [LPC804 Family](#)
- [LPC81x Family](#)
- [LPC82x Family](#)
- [LPC83x Family](#)
- [LPC84x Family](#)
- [LPC8N04 Family](#)

LPC802 Family

[LPC802M001JDH16](#)

[LPC802M001JDH20](#)

[LPC802M011JDH20](#)

[LPC802M001JHI33](#)

[LPC802UK](#)

LPC802M001JDH16

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
Family	LPC800
Sub Family	LPC802
Loader File	\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC802M001JDH16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS
 LPC802M001JDH16
 LPC802M001JDH16_DEVICE
 __LPC800_FAMILY
 __LPC802_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include
 DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
 DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
 DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s
 DeviceFamily=LPC800
 DeviceSubFamily=LPC802

LPC802M001JDH20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
Family	LPC800
Sub Family	LPC802
Loader File	\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC802M001JDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS
 LPC802M001JDH20
 LPC802M001JDH20_DEVICE
 __LPC800_FAMILY
 __LPC802_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include
 DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
 DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
 DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s
 DeviceFamily=LPC800
 DeviceSubFamily=LPC802

LPC802M011JDH20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
Family	LPC800
Sub Family	LPC802
Loader File	\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC802M011JDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS
 LPC802M011JDH20
 LPC802M011JDH20_DEVICE
 __LPC800_FAMILY
 __LPC802_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include
 DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
 DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
 DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s
 DeviceFamily=LPC800
 DeviceSubFamily=LPC802

LPC802M001JHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
Family	LPC800
Sub Family	LPC802
Loader File	\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC802M001JHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS
 LPC802M001JHI33
 LPC802M001JHI33_DEVICE
 __LPC800_FAMILY
 __LPC802_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include
 DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
 DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
 DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s
 DeviceFamily=LPC800
 DeviceSubFamily=LPC802

LPC802UK

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
Family	LPC800
Sub Family	LPC802
Loader File	\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC802UK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS
 LPC802UK
 LPC802UK_DEVICE
 __LPC800_FAMILY
 __LPC802_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x100007FF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include
 DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC802.h
 DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC802_Small_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC802_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC802.c
 DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC802_Vectors.s
 DeviceFamily=LPC800
 DeviceSubFamily=LPC802

LPC804 Family

[LPC804M101JDH20](#)

[LPC804M101JDH24](#)

[LPC804M111JDH24](#)

[LPC804M101JHI33](#)

[LPC804UK](#)

LPC804M101JDH20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c
Family	LPC800
Sub Family	LPC804
Loader File	\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC804M101JDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC804M101JDH20

LPC804M101JDH20_DEVICE

__LPC800_FAMILY

__LPC804_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC804

LPC804M101JDH24

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c
Family	LPC800
Sub Family	LPC804
Loader File	\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC804M101JDH24_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC804M101JDH24

LPC804M101JDH24_DEVICE

__LPC800_FAMILY

__LPC804_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC804

LPC804M111JDH24

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c
Family	LPC800
Sub Family	LPC804
Loader File	\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC804M111JDH24_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC804M111JDH24

LPC804M111JDH24_DEVICE

__LPC800_FAMILY

__LPC804_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC804

LPC804M101JHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c
Family	LPC800
Sub Family	LPC804
Loader File	\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC804M101JHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC804M101JHI33
LPC804M101JHI33_DEVICE
__LPC800_FAMILY
__LPC804_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC804_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC804_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC804
```

LPC804UK

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c
Family	LPC800
Sub Family	LPC804
Loader File	\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC804UK_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC804UK

LPC804UK_DEVICE

__LPC800_FAMILY

__LPC804_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC804.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC804_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC804_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC804.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC804_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC804

LPC81x Family

[LPC810M021FN8](#)

[LPC811M001JDH16](#)

[LPC812M101JDH16](#)

[LPC812M101JD20](#)

[LPC812M101JDH20](#)

[LPC812M101JTB16](#)

LPC810M021FN8

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
Family	LPC800
Sub Family	LPC81x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC81x_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC810M021FN8_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC810_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC810M021FN8
LPC810M021FN8_DEVICE
__LPC800_FAMILY
__LPC81x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00000FFF
RAM	0x10000000 - 0x100003FF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC81x_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC810_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC81x
```

LPC811M001JDH16

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
Family	LPC800
Sub Family	LPC81x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC81x_Small_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC811M001JDH16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC811_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS
 LPC811M001JDH16
 LPC811M001JDH16_DEVICE
 __LPC800_FAMILY
 __LPC81x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x10000000 - 0x100007FF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include
 DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
 DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC81x_Small_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC811_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
 DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s
 DeviceFamily=LPC800
 DeviceSubFamily=LPC81x

LPC812M101JDH16

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
Family	LPC800
Sub Family	LPC81x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC812M101JDH16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC812M101JDH16

LPC812M101JDH16_DEVICE

__LPC800_FAMILY

__LPC81x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC81x

LPC812M101JD20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
Family	LPC800
Sub Family	LPC81x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC812M101JD20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC812M101JD20
LPC812M101JD20_DEVICE
__LPC800_FAMILY
__LPC81x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC812_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC81x
```

LPC812M101JDH20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
Family	LPC800
Sub Family	LPC81x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC812M101JDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC812M101JDH20

LPC812M101JDH20_DEVICE

__LPC800_FAMILY

__LPC81x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC81x

LPC812M101JTB16

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c
Family	LPC800
Sub Family	LPC81x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC812M101JTB16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC812M101JTB16

LPC812M101JTB16_DEVICE

__LPC800_FAMILY

__LPC81x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC81x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC812_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8xx.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC81x_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC81x

LPC82x Family

[LPC824M201JHI33](#)

[LPC824M201JDH20](#)

[LPC822M101JHI33](#)

[LPC822M101JDH20](#)

LPC824M201JHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c
Family	LPC800
Sub Family	LPC82x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC824M201JHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC824_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC824_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC824M201JHI33

LPC824M201JHI33_DEVICE

__LPC800_FAMILY

__LPC82x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC824_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC824_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC82x

LPC824M201JDH20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c
Family	LPC800
Sub Family	LPC82x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC824M201JDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC824_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC824_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC824M201JDH20
LPC824M201JDH20_DEVICE
__LPC800_FAMILY
__LPC82x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10001FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC824_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC824_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC82x
```

LPC822M101JHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c
Family	LPC800
Sub Family	LPC82x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC822M101JHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC822_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC822_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC822M101JHI33

LPC822M101JHI33_DEVICE

__LPC800_FAMILY

__LPC82x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC822_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC822_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC82x

LPC822M101JDH20

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c
Family	LPC800
Sub Family	LPC82x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC822M101JDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC822_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC822_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
LPC822M101JDH20	
LPC822M101JDH20_DEVICE	
__LPC800_FAMILY	
__LPC82x_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
DeviceLoaderFile	\$(TargetsDir)/LPC800/Loader/LPC82x_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC800/XML/LPC822_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC82x.c
DeviceVectorsFile	\$(TargetsDir)/LPC800/Source/LPC822_Vectors.s
DeviceFamily	LPC800
DeviceSubFamily	LPC82x

LPC83x Family

[LPC834M101FHI33](#)

[LPC832M101FDH20](#)

LPC834M101FHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC83x.c
Family	LPC800
Sub Family	LPC83x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC83x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC834M101FHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC834_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC834_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC834M101FHI33

LPC834M101FHI33_DEVICE

__LPC800_FAMILY

__LPC83x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC83x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC834_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC83x.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC834_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC83x

LPC832M101FDH20

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC83x.c
Family	LPC800
Sub Family	LPC83x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC83x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC832M101FDH20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC832_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC832_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC832M101FDH20
LPC832M101FDH20_DEVICE
__LPC800_FAMILY
__LPC83x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x10000000 - 0x10000FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8xx.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC83x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC832_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC83x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC832_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC83x
```

LPC84x Family

[LPC844M201JBD48](#)

[LPC844M201JBD64](#)

[LPC844M201JHI33](#)

[LPC844M201JHI48](#)

[LPC845M301JBD48](#)

[LPC845M301JBD64](#)

[LPC845M301JHI33](#)

[LPC845M301JHI48](#)

LPC844M201JBD48

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC844M201JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC844_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC844M201JBD48

LPC844M201JBD48_DEVICE

__LPC800_FAMILY

__LPC84x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC844_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC844_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC84x

LPC844M201JBD64

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC844M201JBD64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC844_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC844M201JBD64
LPC844M201JBD64_DEVICE
__LPC800_FAMILY
__LPC84x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC844_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC84x
```

LPC844M201JHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC844M201JHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC844_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC844M201JHI33
LPC844M201JHI33_DEVICE
__LPC800_FAMILY
__LPC84x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC844_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC84x
```

LPC844M201JHI48

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC844M201JHI48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC844_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC844M201JHI48
LPC844M201JHI48_DEVICE
__LPC800_FAMILY
__LPC84x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10001FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC844_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC844_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC84x
```

LPC845M301JBD48

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC845M301JBD48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC845_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC845_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC845M301JBD48
LPC845M301JBD48_DEVICE
__LPC800_FAMILY
__LPC84x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10003FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC845_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC845_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC84x
```

LPC845M301JBD64

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC845M301JBD64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC845_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC845_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC845M301JBD64

LPC845M301JBD64_DEVICE

__LPC800_FAMILY

__LPC84x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC845_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC845_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC84x

LPC845M301JHI33

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC845M301JHI33_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC845_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC845_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM0PLUS
LPC845M301JHI33
LPC845M301JHI33_DEVICE
__LPC800_FAMILY
__LPC84x_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10003FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile=$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
DeviceLoaderFile=$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC800/XML/LPC845_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
DeviceVectorsFile=$(TargetsDir)/LPC800/Source/LPC845_Vectors.s
DeviceFamily=LPC800
DeviceSubFamily=LPC84x
```

LPC845M301JHI48

Device Details

CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c
Family	LPC800
Sub Family	LPC84x
Loader File	\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC845M301JHI48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC845_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC845_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

LPC845M301JHI48

LPC845M301JHI48_DEVICE

__LPC800_FAMILY

__LPC84x_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x10000000 - 0x10003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC800/CMSIS/Device/Include

DeviceHeaderFile=\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC84x.h

DeviceLoaderFile=\$(TargetsDir)/LPC800/Loader/LPC84x_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC800/XML/LPC845_Registers.xml

DeviceSystemFile=\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC84x.c

DeviceVectorsFile=\$(TargetsDir)/LPC800/Source/LPC845_Vectors.s

DeviceFamily=LPC800

DeviceSubFamily=LPC84x

LPC8N04 Family

[LPC8N04FHI24](#)

LPC8N04FHI24

Device Details	
CMSIS Header File	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8N04.h
CMSIS Include Path	\$(TargetsDir)/LPC800/CMSIS/Device/Include
CMSIS System File	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8N04.c
Family	LPC800
Sub Family	LPC8N04
Loader File	\$(TargetsDir)/LPC800/Loader/LPC8N04_Loader.elf
Memory Map File	\$(TargetsDir)/LPC800/XML/LPC8N04FHI24_MemoryMap.xml
Register Definition File	\$(TargetsDir)/LPC800/XML/LPC8N04_Registers.xml
Vectors File	\$(TargetsDir)/LPC800/Source/LPC8N04_Vectors.s
Preprocessor Definitions	
ARM_MATH_CM0PLUS	
LPC8N04FHI24	
LPC8N04FHI24_DEVICE	
__LPC800_FAMILY	
__LPC8N04_SUBFAMILY	
Memory Segments	
FLASH	0x00000000 - 0x000077FF
RAM	0x10000000 - 0x10001FFF
Project Macros	
DeviceIncludePath	\$(TargetsDir)/LPC800/CMSIS/Device/Include
DeviceHeaderFile	\$(TargetsDir)/LPC800/CMSIS/Device/Include/LPC8N04.h
DeviceLoaderFile	\$(TargetsDir)/LPC800/Loader/LPC8N04_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/LPC800/XML/LPC8N04_Registers.xml
DeviceSystemFile	\$(TargetsDir)/LPC800/CMSIS/Device/Source/system_LPC8N04.c
DeviceVectorsFile	\$(TargetsDir)/LPC800/Source/LPC8N04_Vectors.s
DeviceFamily	LPC800
DeviceSubFamily	LPC8N04